

```

016      porta    equ    00h
017      portb    equ    02h
018      portc    equ    04h
019      creg     equ    06h
020
021      ;initializing the ports of 8253a
022      mov      al, 88h
023      out      creg, al
024
025 a0:      mov      al, 00h
026      out      portc, al
027
028 a1:      in       al, portc
029      and      al, 0f0h
030      cmp      al, 0f0h      ;check for key release
031      jnz      a1
032      call     delay20
033      mov      al, 00h
034      out      portc, al
035 a2:      in       al, portc
036      and      al, 0f0h
037      cmp      al, 0f0h
038      jz       a2
039      call     delay20
040
041      ;validity of key press
042      mov      al, 00h
043      out      portc, al
044      in       al, portc
045      and      al, 0f0h
046      cmp      al, 0f0h
047      jz       a2
048
049      ;key press column 1
050      mov      al, 0eh
051      mov      bl, al
052      out      portc, al

```

 程序 (核心)
  仿真 (核心)
  开发资料
  软件教程
  文档说明
  注意事项
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  原理图.pdf